

SysML for Systems Engineering

This course is designed for systems engineers who are new to SysML. Delegates are taught how to create the core SysML diagram types to analyse requirements and create static and dynamic design models. Practical exercises help to reinforce the theory.

Duration: 3 days

Prerequisites: No prior SysML experience is required.
A background in systems engineering is helpful.

Equipment:



For on-site course delivery the customer should provide a suitable training room with a projector to connect to our trainer's laptop and a whiteboard or flipchart. All exercises are completed on paper therefore no PCs or laptops are required.



For webinar training sessions delegates need access to a PC or laptop with Internet connection and VoIP (a headset can be helpful for privacy in an office environment).

Course Style:


































50% theory, 50% practical.

Each module is accompanied by targeted exercises to allow delegates to apply the theory and become confident with new concepts and notation.

Delegate Handouts:

Each delegate receives a folder containing all the course slides and comprehensive theory notes which form excellent reference material. Folders also contain exercises and suggested solutions. Following successful completion of the course each delegate receives a certificate.

Course Modules:

| | | | Theory | EA | Notation | Exercise | Hands-on |
|----------|---------------------------------|----------|---|----|---|---|----------|
| HIPPO 00 | Introduction | ½ hour | | | | | |
| HIPPO 30 | SysML Overview | ½ hour |  | |  | | |
| HIPPO 31 | SysML Requirements | ½ hour |  | |  | | |
| HIPPO 12 | SysML Requirements Elicitation | ½ hour |  | |  | | |
| HIPPO 13 | SysML Use Case Essentials | 2½ hours |  | |  |  | |
| HIPPO 14 | SysML Use Case Advanced | 2 hours |  | |  |  | |
| HIPPO 32 | SysML Block Definition Diagrams | 1½ hours |  | |  |  | |
| HIPPO 33 | SysML Internal Block Diagrams | 1 hour |  | |  |  | |
| HIPPO 34 | SysML Parametric Diagrams | 1½ hours |  | |  |  | |
| HIPPO 15 | SysML Activity Diagrams | 1 hour |  | |  |  | |
| HIPPO 23 | SysML Sequence Diagrams | 2 hours |  | |  |  | |
| HIPPO 25 | SysML State Machine Essentials | 1½ hours |  | |  |  | |
| HIPPO 26 | SysML State Machine Advanced | 1 hour |  | |  |  | |