SysML for Systems Engineering 🔷 🕬 🌽





This course is designed for systems engineers who are new to SysML. Delegates are taught how to create the core SysML diagram types to analyse requirements and create static and dynamic design models. Practical exercises help to reinforce the theory.

Duration: 3 days

Prerequisites: No prior SysML experience is required.

A background in systems engineering is helpful.

Equipment:

For on-site delivery the customer should provide a suitable training room with a screen or projector to connect to our trainer's laptop and a whiteboard or flipchart. All exercises are completed on paper therefore no PCs or laptops are required.



For webinar delivery delegates require a PC or laptop with an Internet connection (a headset can be helpful). If you wish to test your environment join a test WebEx meeting: www.webex.com/test-meeting.html

Course Style: 50% theory, 50% practical.

Each module is accompanied by targeted exercises to allow delegates to apply the theory and

become confident with new concepts and notation.

Delegate Handouts: Each delegate receives a folder containing all the course slides and comprehensive theory

notes which form excellent reference material. Folders also contain exercises and suggested solutions. Following successful completion of the course each delegate receives a certificate.

Course Modules:			Theory	EA	Notation	Exercise	Hands-on
HIPPO 00	Introduction	½ hour					
HIPPO 30	SysML Overview	½ hour			Systal		
HIPPO 31	SysML Requirements	½ hour			Systyl		
HIPPO 12	SysML Requirements Elicitation	½ hour			Systyl		
HIPPO 13	SysML Use Case Essentials	2½ hours			Systyl	S	
HIPPO 14	SysML Use Case Advanced	2 hours			Systyl	S	
HIPPO 32	SysML Block Definition Diagrams	1½ hours			Systyl	N	
HIPPO 33	SysML Internal Block Diagrams	1 hour			Systyl	N	
HIPPO 34	SysML Parametric Diagrams	1½ hours			Systyl	N.	
HIPPO 15	SysML Activity Diagrams	1 hour			Systyl	S	
HIPPO 23	SysML Sequence Diagrams	2 hours			Systyl	S	
HIPPO 25	SysML State Machine Essentials	1½ hours			Systyl	S	
HIPPO 26	SysML State Machine Advanced	1 hour			Systyl	N	



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